

Abstracts

High performance flip-chip multi-mode, multi-band VCO IC using a standard low cost BiCMOS process

D. Lovelace and R. Disilvestro. "High performance flip-chip multi-mode, multi-band VCO IC using a standard low cost BiCMOS process." 2001 Radio Frequency Integrated Circuits (RFIC) Symposium 01. (2001 [RFIC]): 57-60.

A low cost, high performance flip-chip multi-mode, multi-band voltage controlled oscillator (VCO) IC is presented. This IC is suitable for high performance VCO circuits over a wide range of frequencies. A novel noise reduction circuit technique is used to obtain improved phase noise performance without the use of a higher cost SiGe BiCMOS processes. This IC has a measured phase noise of -162 dBc/Hz at a 20 MHz offset operating at an oscillation frequency of 900 MHz with an output power of +10 dBm.

 [Return to main document.](#)